

Supported Models by Asic.To Firmware

X21 Series

Model	Minimum Supported Power/Hashrate	Maximum Supported Power/Hashrate
S21 Pro (65 Chips Per Chain)	2280 ^W / 152 TH	4690 ^W / 313 TH
S21-Imm (156 Chips Per Chain)	1630 ^W / 92 TH	6960 ^W / 387 TH
S21 (108 Chips Per Chain)	1960 ^W / 113 TH	4230 ^W / 243 TH
T21 (108 Chips Per Chain)	2260 ^W / 113 TH	5600 ^W / 280 TH
S21-Hydro (216 Chips Per Chain)	2500 ^W / 148 TH	6300 ^W / 383 TH

X19 Series

Model	Minimum Supported Power/Hashrate	Maximum Supported Power/Hashrate
S19 (76 Chips Per Chain)	2100 ^W / 78 TH	4960 ^W / 124 TH
T19 (76 Chips Per Chain)	2400 ^W / 70 TH	4973 ^W / 120 TH
S19 – 88 (88 Chips Per Chain)	2750 ^W / 84 TH	4000 ^W / 110 TH
S19 – 126 (126 Chips Per Chain)	1630 ^W / 67 TH	4700 ^W / 130 TH
S19 A (72 Chips Per Chain)	2710 ^W / 90 TH	3640 ^W / 110 TH
S19 A Pro (100 Chips Per Chain)	2800 ^W / 100 TH	4520 ^W / 138 TH
S19 Hydro (104 Chips Per Chain)	2700 ^W / 89 TH	9500 ^W / 218 TH
T19 Hydro (104 Chips Per Chain)	3560 ^W / 95 TH	7270 ^W / 194 TH
S19 Pro (114 Chips Per Chain)	2450 ^W / 80 TH	6200 ^W / 161 TH
S19 Pro A (114 Chips Per Chain)	3500 ^W / 110 TH	6200 ^W / 161 TH
S19 Pro Hydro (180 Chips Per Chain)	3495 ^W / 132 TH	7700 ^W / 250 TH
S19 Pro Plus Hydro (180 Chips Per Chain)	2750 ^W / 126 TH	7500 ^W / 250 TH
S19 XP (110 Chips Per Chain)	2100 ^W / 97 TH	5700 ^W / 200 TH
S19 K Pro (77 Chips Per Chain)	1500 ^W / 80 TH	4250 ^W / 183 TH
S19 XP Hydro (204 Chips Per Chain)	4200 ^W / 200 TH	6350 ^W / 282 TH
S19 Plus (80 Chips Per Chain)	2850 ^W / 78 TH	3900 ^W / 110 TH
S19i (80 Chips Per Chain)	2700 ^W / 82 TH	4300 ^W / 1125 TH
S19j (108 Chips Per Chain)	3050 ^W / 82 TH	4900 ^W / 114 TH

S19j Plus (126 Chips Per Chain)	2340 ^W / 74 TH	7000 ^W / 148 TH
S19j Pro (126 Chips Per Chain)	1740 ^W / 65 TH	4880 ^W / 140 TH
S19j Pro A (126 Chips Per Chain)	1740 ^W / 65 TH	4880 ^W / 140 TH
S19j Pro Plus (120 Chips Per Chain)	1900 ^W / 80 TH	5900 ^W / 159 TH

L7 Series

Model	Minimum Supported Power/Hashrate	Maximum Supported Power/Hashrate
L7 (120 Chips Per Chain)	1855 ^W / 4800 ^{MH}	6000 ^W / 12000 ^{MH}

X17 Series

Model	Minimum Supported Power/Hashrate	Maximum Supported Power/Hashrate
T17 (30 Chips Per Chain)	2000 ^W / 42 TH	3000 ^W / 55 TH
T17 Plus (44 Chips Per Chain)	2300 ^W / 55 TH	3700 ^W / 75 TH
S17 / S17 Pro (48 Chips Per Chain)	2000 ^W / 53 TH	3600 ^W / 80 TH
S17 Plus (65 Chips Per Chain)	2600 ^W / 70 TH	4100 ^W / 95 TH

X9 Series

Model	Minimum Supported Power/Hashrate	Maximum Supported Power/Hashrate
S9 / S9i / S9j (63 Chips Per Chain)	750 ^W / 10 TH	1700 ^W / 18 TH
S9 Dual (63 Chips Per Chain)	750 ^W / 10 TH	1700 ^W / 21 TH
T9 Plus (63 Chips Per Chain)	750 ^W / 10 TH	1450 ^W / 16 TH

L3 Series

Model	Minimum Supported Power/Hashrate	Maximum Supported Power/Hashrate
L3 / L3 Plus (72 Chips Per Chain)	620 ^W / 500 ^{MH}	1285 ^W / 715 ^{MH}